Modelling on c-Si/a-Si:H wire solar cells: some key parameters to optimize the photovoltaic performance

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Abstract Solar cells based on silicon nano- or micro-wires have attracted much attention as a promising path for low cost photovoltaic technology. The key point of this structure is the decoupling of the light absorption from the carriers collection. In order to predict and optimize the performance potential of p- (or n-) doped c-Si/ n- (or p-) doped a-Si:H nanowire-based solar cells, we have used the Silvaco-Atlas software to model a single-wire device. In particular, we have noticed a drastic decrease of the open-circuit voltage ($V_{oc}$) when increasing the doping density of the silicon core beyond an optimum value. We present here a detailed study of the parameters that can alter the $V_{oc}$ of c-Si(p)/a-Si:H (n) wires according to the doping density in c-Si. A comparison with simulation results obtained on planar c-Si/a-Si:H heterojunctions shows that the drop in $V_{oc}$, linked to an increase of the dark current in both structures, is more pronounced for radial junctions due to geometric criteria. These numerical modelling results have lead to a better understanding of transport phenomena within the wire.

1 Introduction

Solar cells based on Si micro- or nanowire arrays with radial p-n junction are of great interest as potentially low-cost solutions in solar cell production and have been extensively studied in the last years [1–6]. Unlike planar structures, photon absorption and minority carrier collection are decoupled as shown in Figure 1, and are not anymore in competition when optimizing the cell dimensions. In fact, the wires can be grown long enough to optimize light absorption while being small enough in radius to collect photo-generated carriers. This last property enables the use of low purity Si substrates with a short minority carrier diffusion length.

The vapour-liquid-solid (VLS) technique [7] assisted by gold catalyst is one of the most current methods to grow micro- and nano-wires for photovoltaic applications. Despite many advantages in using Au as VLS catalyst (non toxicity, chemical stability, availability), Au is known to diffuse in the nanowires [8] creating deep-level defects in Si and leading to an increase of the carrier recombination rate. Among the large number of catalyst materials that have been tested to replace Au [9], aluminium is a good candidate since it does not create deep-level defects. Nevertheless, Al is a p-type dopant and the wires can be strongly p-doped. It is not obvious to know whether the performances of the solar device are strongly influenced by such a p-type doping level. In that case, numerical modelling can be of great help. In this modelling work, we have studied one single-wire Si solar cell which consists in a p-type crystalline silicon (c-Si) core surrounded by a n-type hydrogenated amorphous silicon (a-Si:H) thin layer. Cell efficiency, open-circuit voltage ($V_{oc}$), short-circuit current ($I_{sc}$) and fill factor (FF) under AM 1.5 spectrum have been calculated as a function of the p-type doping density ($N_a$) in the core. A drastic decrease in $V_{oc}$ has been observed when $N_a$ reaches values higher than $1 \times 10^{17}$ cm$^{-3}$. An insight into the transport properties of the wire in the dark can help to understand this behavior.
2 Silicon nanowire modelling: geometry and physical parameters

Simulations of the electrical characteristics of a single-wire c-Si/a-Si:H radial heterojunction solar cell were performed using ATLAS Silvaco software [10]. The geometry of the radial p-n junction studied here is given in Figure 2. The design was defined as a simulation plane in 2-dimensional cylindrical coordinates which models the 3D cylindrical structure. The wire dimensions were chosen according to experimental data of fabricated wire array cells, with a wire length of 25 μm and a radius of 810 nm. The p-type c-Si core (radius of 800 nm) is surrounded by an n-type doped a-Si:H. Ohmic contacts were used, one at the bottom of the structure on the crystalline base and another one around the wire on the amorphous layer. At the bottom contact, the electron surface recombination velocity has been taken equal to 50 cm s$^{-1}$. For the surrounded contact, flat band conditions were imposed.

For the a-Si:H layer, the distribution of the density of states (DOS) is made of two exponential band tails (valence and conduction band tails) and two Gaussian distributions of deep defect states, one donor-like and one acceptor-like. The DOS and doping concentration were adjusted such that the Fermi level at room temperature was set at 0.2 eV below the conduction band edge $E_c$. The electron and hole mobilities were taken equal to $\mu_n = 20$ cm$^2$ V$^{-1}$ s$^{-1}$ and $\mu_p = 5$ cm$^2$ V$^{-1}$ s$^{-1}$, respectively. In the c-Si core, we fixed a value of 1 ms for the carrier lifetime which corresponds to a diffusion length much bigger than the wire radius. The doping density $N_a$ was varied between $1 \times 10^{15}$ cm$^{-3}$ and $1 \times 10^{19}$ cm$^{-3}$ such that the Fermi level at room temperature moves from 0.25 eV above the valence band edge $E_v$ to a position very close to $E_v$, respectively.

The simulation is based on the resolution of Poisson’s equation and electron and hole continuity equations. The Boltzmann statistics is used with the drift-diffusion model in ATLAS, and Shockley-Read-Hall recombination was considered. The AM1.5 solar spectrum was used for the optical generation, the wire being under vertical illumination (see Fig. 2). Current-voltage characteristics ($I(V)$) were calculated in the dark and under standard one-sun illumination conditions. The open-circuit voltage, the short-circuit current, and the efficiency of the cell were deduced from the $I(V)$ curves under illumination.

3 Simulation results and discussion

The variations of the efficiency and of the open-circuit voltage of the single-wire device is given as a function of the doping density $N_a$ in Figures 3a and 3b, respectively. These variations are compared to those calculated for an equivalent c-Si(p)/a-Si:H(n) planar structure.
Boltzmann’s constant, electronic charge and different doping density (observed, which indicates two current regimes: at high forward bias, the dark current which, at such Na values, mainly becomes a recombination current in the defect-states rich a-Si:H layer. For high doping density (Na > 1 × 10^{17} cm^{-3}), no part of the I_{dark}(V) characteristic at forward bias can be fitted by the previous exponential law and the recombination current in the a-Si:H shell becomes predominant all over the forward bias range. This is illustrated in Figure 5 where I_{dark}(V) characteristics are calculated for various defect density N_{defect} in the a-Si:H layer and a given doping density Na equal to 1 × 10^{15} cm^{-3}. It can be observed that the dark current in the wire decreases when N_{defect} increases.

The heavy drop in Voc at high doping density of the c-Si(p) core is directly related to a strong increase of the dark current which, at such Na values, mainly becomes a recombination current in the defect-states rich a-Si:H layer. However, according to Figure 3b, this behavior is much more pronounced for the radial junction than for the planar one (it has to be reminded that this comparison is presented for the same illuminated area in both structures). A mapping of the dark current components near the c-Si(p)/a-Si:H(n) heterointerface of the single wire is presented in Figure 6 for Na = 1 × 10^{17} cm^{-3} and Na = 1 × 10^{19} cm^{-3}. It can be observed that for the lowest Na value, most of the current is concentrated at the bottom interface of the structure whereas it is homogenously spread all over the junction for Na = 1 × 10^{19} cm^{-3}. Simulations have shown that this difference in the current distribution appears around Na = 1 × 10^{17} cm^{-3}. Consequently, the active junction surface that really contributes to the dark current becomes wider when Na increases and it is expected that the ratio between the radius R of the wire (related to the illuminated area) and the wirelength L (related to the p-n junction) will affect the photovoltaic performance of the wire solar cell as soon as high doping density in the core is reached. This is also illustrated in Figure 7 where the dark current characteristics are plotted for two wirelength values L = 25 μm and L = 250 μm. For Na = 1 × 10^{17} cm^{-3}, the high forward bias dark current does not depend on the wirelength.

**Fig. 4.** Forward dark current characteristics I_{dark}(V) for different doping density Na in the c-Si(p) core of the wire. For Na ≤ 1 × 10^{17} cm^{-3}, the fits of the curves with the exponential law I_{dark} = I_0 \exp(qV/kT) corresponding to an ideal diode behavior is also given (dash lines).

**Fig. 5.** Forward dark current characteristics I_{dark}(V) for various values of the defect density N_{defect} in the a-Si:H layer and a given doping density Na in the c-Si(p) core equal to 1 × 10^{15} cm^{-3}.
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Fig. 6. Mapping of the dark current density distribution near the c-Si(p)/a-Si:H(n) heterointerface of the wire for \( N_a = 1 \times 10^{17} \text{ cm}^{-3} \) and \( N_a = 1 \times 10^{19} \text{ cm}^{-3} \). Most of the current is concentrated at the bottom interface for the lowest value of \( N_a \) whereas it is homogeneously spread all over the p-n junction for the highest one.

![Diagram](image)

Fig. 7. Forward dark current characteristics \( I_{\text{dark}}(V) \) for two wirelength values (\( L = 25 \mu\text{m} \) and \( L = 250 \mu\text{m} \)) at \( N_a = 1 \times 10^{17} \text{ cm}^{-3} \) and \( N_a = 1 \times 10^{19} \text{ cm}^{-3} \).

whereas it increases with \( L \) for \( N_a = 1 \times 10^{19} \text{ cm}^{-3} \) due to an homogenous current distribution all over the wirelength. Thus, at high values of \( N_a \), we should obtain a comparable decrease in \( V_{\text{oc}} \) between a planar and a radial junction for an aspect ratio (ratio between the illuminated area and the junction area) of the wire equal to 1. In the wire structures we have studied, the junction area is bigger than the illuminated area (ratio \( R/L \) much smaller than 1) such that the drop in \( V_{\text{oc}} \) with an increasing doping density is much more pronounced for the single-wire cell than for the planar one.

At low doping density, the current is dominated by the minority carriers diffusion and it mainly runs through a small active surface of the junction at the bottom of the structure as shown in Figure 6. This specific behavior seems to be related to the presence of a highly conductive electron interface layer at the c-Si(p) surface. Indeed, the band offsets between a-Si:H and c-Si can provide a strong band bending which results in a strong inversion layer at the c-Si surface [11–13]. For c-Si(p)/a-Si:H(n) structures, it has been shown from numerical calculations that the inversion layer occurs even at low values of the conduction band offset \( \Delta E_c \) and that the electron concentration in the interface region of c-Si strongly increases with \( \Delta E_c \) as soon as \( \Delta E_c \geq 0.1 \text{ eV} \) [12]. In the case of c-Si(n)/a-Si:H(p) structures, a lower limit of the valence band offset \( \Delta E_v \) has been found equal to 0.25 eV [13] such that \( \Delta E_v \) can be varied in a wider range than \( \Delta E_c \) to observe or not an interface inversion layer. Thus, in
order to establish a link between the current distribution at the wire heterointerface and the presence or not of an inversion surface layer, we have made some calculations on a c-Si(n)/a-Si:H(p) radial cell for different values of the valence band offset. A mapping of the dark current components near the c-Si(n)/a-Si:H(p) interface is given in Figure 8 for $\Delta E_v = 0.42$ eV and $\Delta E_v = 0.15$ eV. It can be observed that the current distribution is homogeneous along the wire for $\Delta E_v = 0.15$ eV whereas it is concentrated at the bottom of the cell for $\Delta E_v = 0.42$ eV which can be explained by a charge accumulation in the inversion layer at the interface. Furthermore, calculations performed on a single wire crystalline homojunction also show an homogeneous current distribution all along the wire interface whatever the doping density in the p-type core as shown in Figure 9. The $I_{dark}(V)$ characteristics follow the exponential law given above with a dependence on the wire length (not shown here). To come back to our c-Si(p)/a-Si:H(n) wire structures, high values of the doping density in the crystalline core lead to a modification of the band bending such that no strong inversion layer occurs. An homogeneous current distribution is thus observed along the wire for $N_a \geq 1 \times 10^{17}$ cm$^{-3}$. Further studies have to be done to explain more precisely the location of the “hot spot” at the bottom of the interface in the case of a non homogeneous current distribution. In particular, the geometry and position of the contacts can play an important role in that feature.

4 Conclusion

We have studied the potential performance of a single c-Si(p)/a-Si:H(n) wire based solar cell through 2D numerical modelling. We have noticed in particular a drastic drop of the open-circuit voltage when the p-type doping density $N_a$ of the wire core increases beyond an optimum value equal to $1 \times 10^{17}$ cm$^{-3}$. This loss in $V_{oc}$ is linked to an increase of the dark current with $N_a$, the transport in the forward bias region being dominated by recombination current in the a-Si:H layer at high values of $N_a$. Moreover, the decrease in $V_{oc}$ is less pronounced for an equivalent planar c-Si(p)/a-Si:H(n) heterojunction, the illuminated area of the planar and the radial modelled structures being the same. This difference can be explained by a dependence of the dark current in the wire with the wire length. Thus, for $N_a \geq 1 \times 10^{17}$ cm$^{-3}$ the wire based solar cell performance strongly depend on the ratio between the radius, which is linked to the illuminated area, and the wire length, which is related to the p-n junction area. In conclusion, this study shows that the doping density $N_a$ of the c-Si core should
Fig. 9. Mapping of the dark current density distribution near the interface of a silicon crystalline homojunction wire with a doping density of the core $N_a = 1 \times 10^{17} \text{ cm}^{-3}$ and $N_a = 1 \times 10^{19} \text{ cm}^{-3}$.

be kept below an optimum value of $1 \times 10^{17} \text{ cm}^{-3}$ and that any VLS catalyst which introduces a higher $p$-type doping density in the core should be avoided.

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